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Reveo-0202USAAON00
10/719,663

FEB 21 2006

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT: Sadeg M. Faris)
SERIAL NO.: 10/719,663) Group Art Unit
FILING DATE: November 20, 2003) 2812
FOR: Method of Fabricating Multi Layer)
Devices on Buried Oxide Layer Substrates) Examiner
TBD

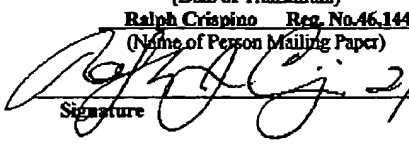
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RESPONSE TO OFFICE ACTION

Sirs:

In response to the Office Action mailed October 18, 2005 in the above application, Applicants respectfully submit the herein election. Applicant hereby petitions for and requests a one -month extension of time to extend the period for response up to and including February 21, 2006 (since Feb. 18 was Saturday, Feb. 19 was Sunday and Feb. 20 was a Federal Holiday). The Commissioner is hereby authorized to charge the \$60.00 extension fee for a small entity to Deposit Account No. 501648. Applicant is a Small Entity.

I hereby certify that this correspondence is being facsimile transmitted to the United States Patent and Trademark Office, facsimile number (571) 273-8300	
February 21, 2006 (Date of Transmittal)	
Ralph Crispino Reg. No. 46,144 (Name of Person Mailing Paper)	
 Signature	2/21/06 Date

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